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APPOINTMENT/REVOCATION OF POWER OF ATTORNEY

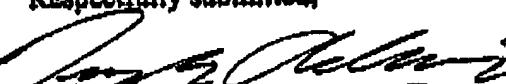
REVOCATION OF POWER OF ATTORNEY: The Assignee of the entire right, title, and interest in the applications and issued patents listed in the attached schedule (Schedule A), Lamina Ceramics, Inc., hereby revokes all previous powers of attorney or authorizations of agents given in the applications and patents set forth in Schedule A.

POWER OF ATTORNEY: The Assignee of the entire right, title, and interest in the applications and patents set forth in Schedule A, Lamina Ceramics, Inc., hereby appoints the following attorneys and/or agents, with full power of substitution and revocation, to transact all business in connection with these applications and patents in the United States Patent and Trademark Office: Glen E. Books (24,950), Stephen R. Buckingham (40,538), Michael B. Johannessen (35,557), Alex R. Pagano (44,994), George D. Morgan (46,505), Justin D. Petruzzelli (52,118), and Daniel D. Sierchio (53,591), all of Lowenstein Sandler PC.

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Lamina Ceramics, Inc. has established its right to take action in the applications and patents set forth in Schedule A by filing herewith a Statement under 37 C.F.R. 3.73(b). The undersigned, Taylor Adair, Chief Executive Officer and President, Lamina Ceramics, Inc., is authorized to sign on behalf of the Assignee.

Respectfully submitted,


Taylor Adair

Chief Executive Officer and President
Lamina Ceramics, Inc.

Date:

SCHEDULE A
APPLICATIONS AND PATENTS TO WHICH THE POWER OF ATTORNEY IS APPLICABLE

| Application Number/Patent Number | Filing Date/Issue Date | First Named Inventor | Title | Group Art Unit | Examiner | Attorney Docket Number |
|----------------------------------|--------------------------|----------------------|---|----------------|--------------|------------------------|
| Serial Number 09/430,642 | Filed: October 29, 1999 | Geller | High Performance Embedded RF Filter | 2817 | S.E. Jones | 14123-58 |
| Patent Number 5,847,935 | Issued: December 8, 1998 | Thaler | Electronic Circuit Chip Package | 2835 | J. Foster | 14123-60 |
| Patent Number 6,300,267 | Issued: October 9, 2001 | Chen | High Dielectric Constant Buried Capacitors with Extended Operating Temperature Ranges | 1755 | D. Brunsmann | 14123-44 |
| Patent Number 5,876,536 | Issued: March 2, 1999 | Kumar | Method for the Reduction of Lateral Shrinkage in Multilayer Circuit Boards on a Substrate | 1734 | M. Mayes | 14123-34 |
| Serial Number 10/284,544 | Filed: October 30, 2002 | Hammond | Method of Making Ceramic Multilayer Circuit Boards Mounted in a Patterned Metal Support Substrate | 3729 | T. Phan | 14123-35 |
| Serial Number 09/146,478 | Filed: September 3, 1998 | Kumar | Method for the Reduction of Lateral Shrinkage in Multilayer Circuit Boards on a Substrate | 1934 | M. Mayes | 14123-24 |